

Figure 1

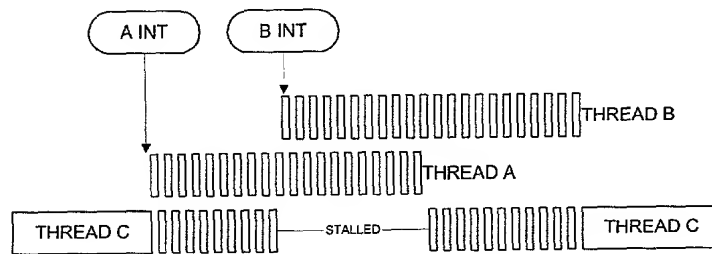


Figure 2

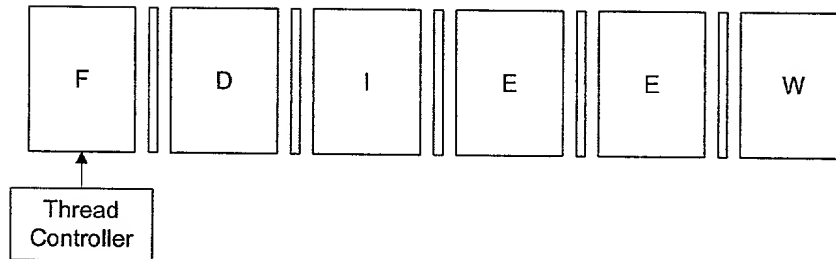


Figure 3

FIG. 3 is a block diagram of a thread state in a multithreaded CPU. The thread state is represented by a sequence of boxes labeled F, D, I, E, E, and W. A Thread Controller box is connected to the F box by an upward arrow.

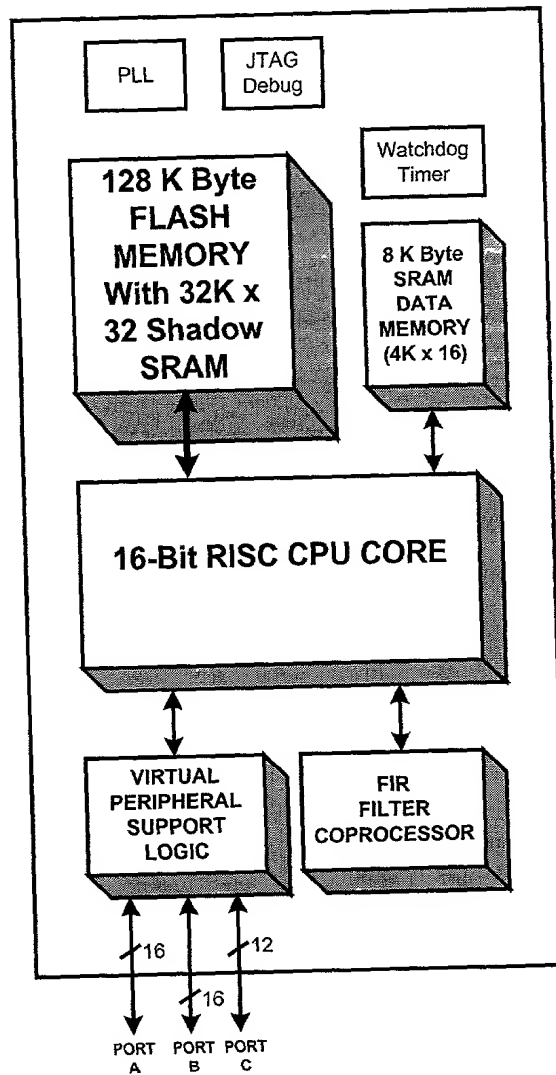


Figure 4

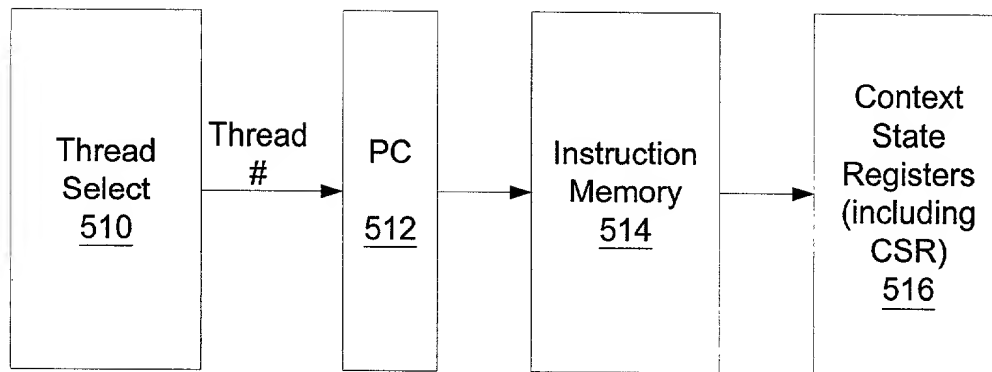


Figure 5A

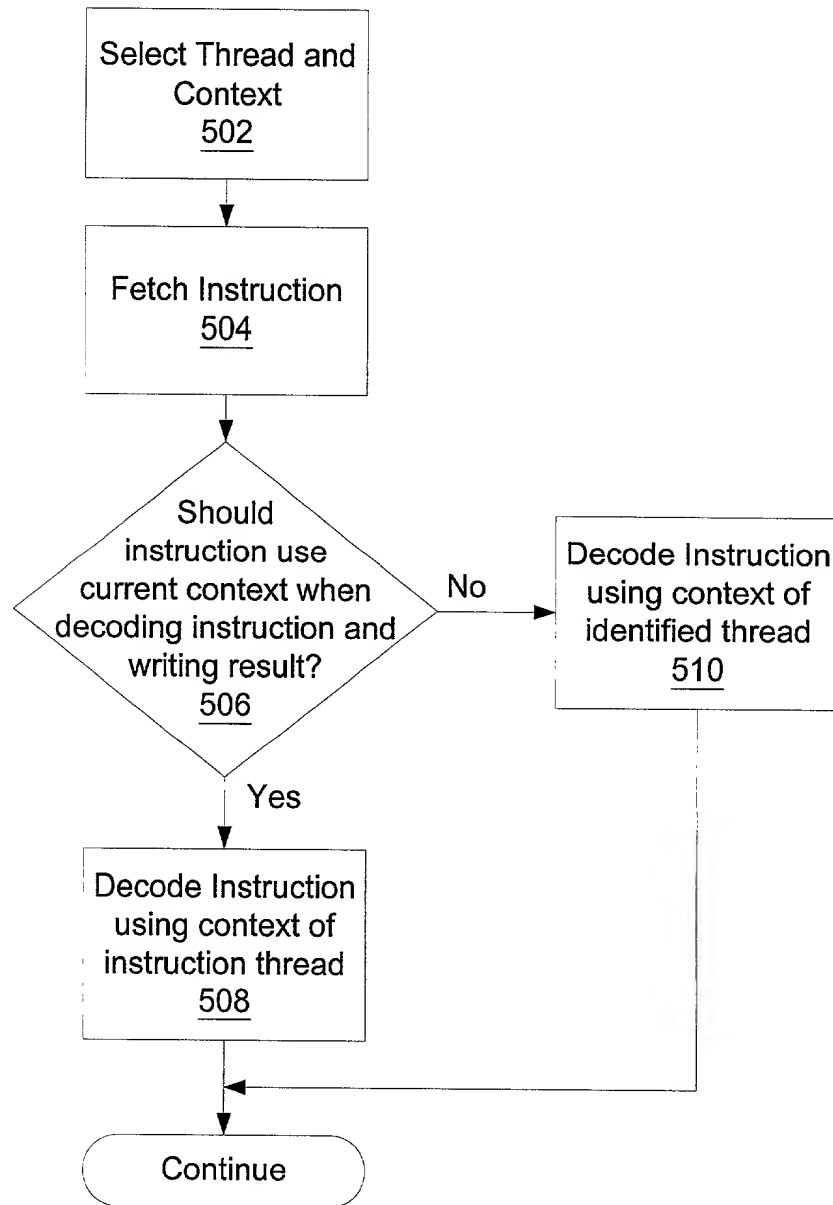


Figure 5B

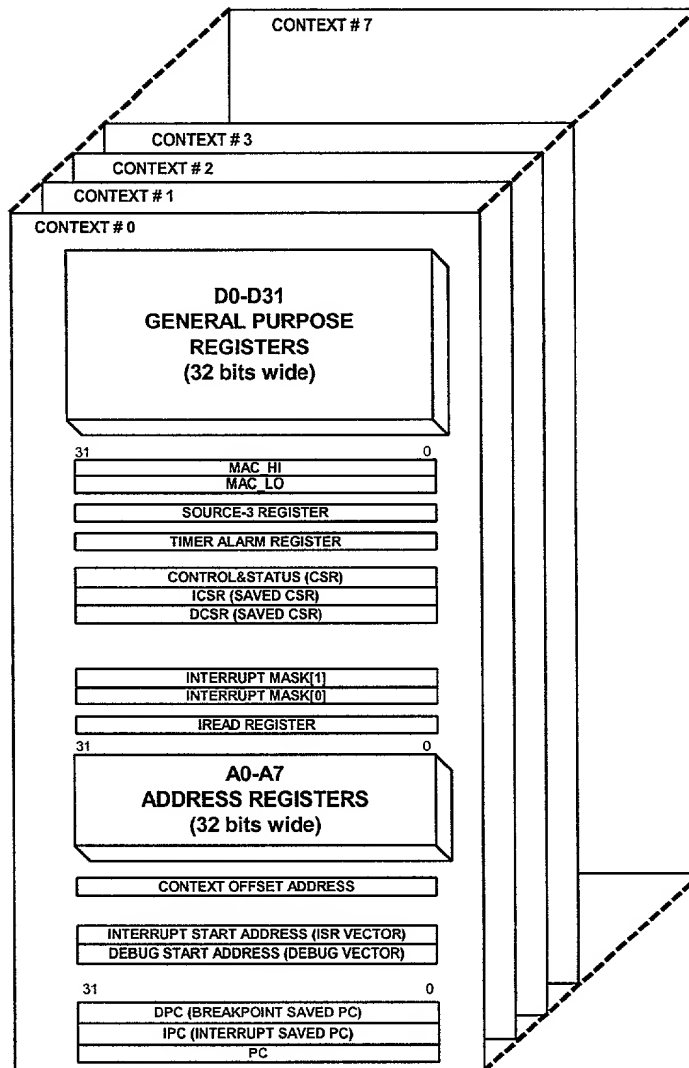


Figure 6A

OCCB- 16	OCCB- 32	ESO	STS	EDO	DTS	Other
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Figure 6B

A	B	A	C
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A	B	A	C	A	B	A	C	A	B	A	C
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→
Time

Figure 7a

A	*	B	*
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A	D	B	E	A	F	B	E	A	F	B	E
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→
Time

Figure 7b

*	*	*	*
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F	E	G	G	G	F	F	E	G	G	E	G
---	---	---	---	---	---	---	---	---	---	---	---

→
Time

Figure 7c

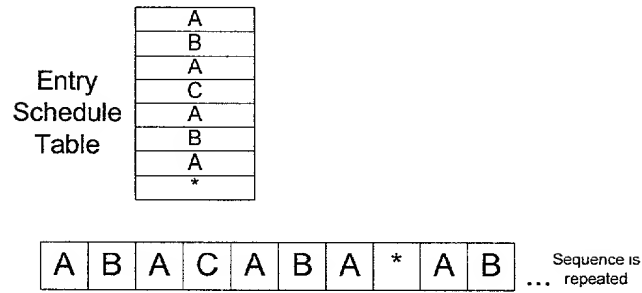


Figure 7d

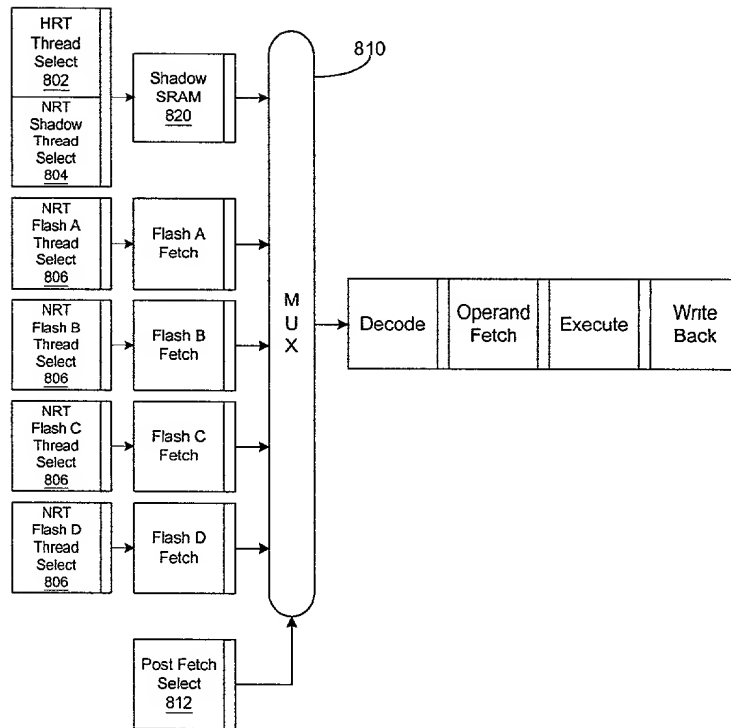


Figure 8

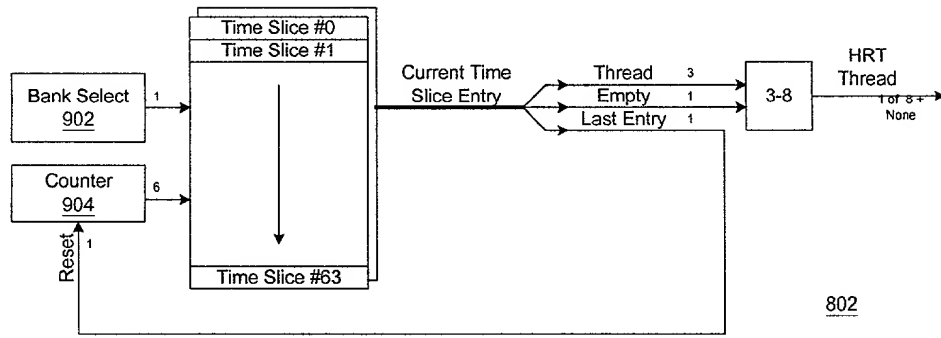


Figure 9

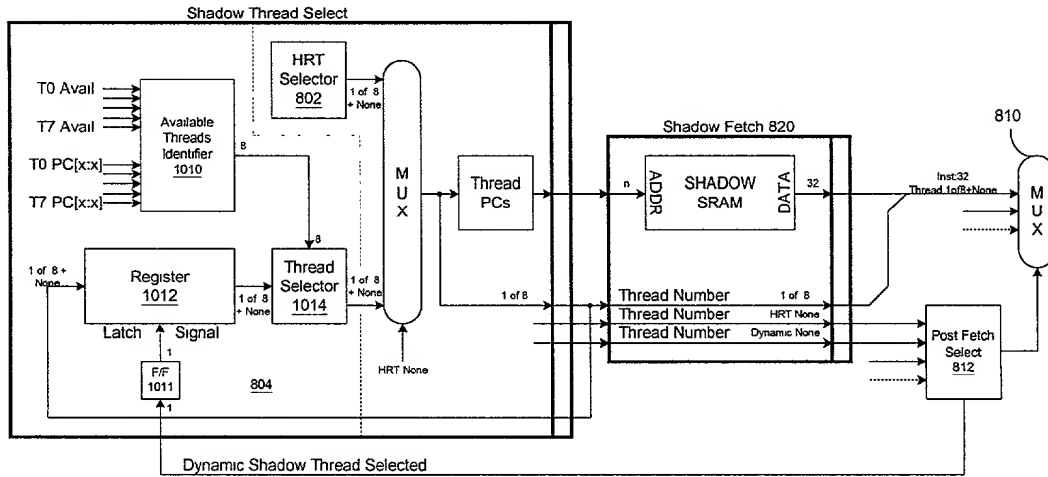


Figure 10

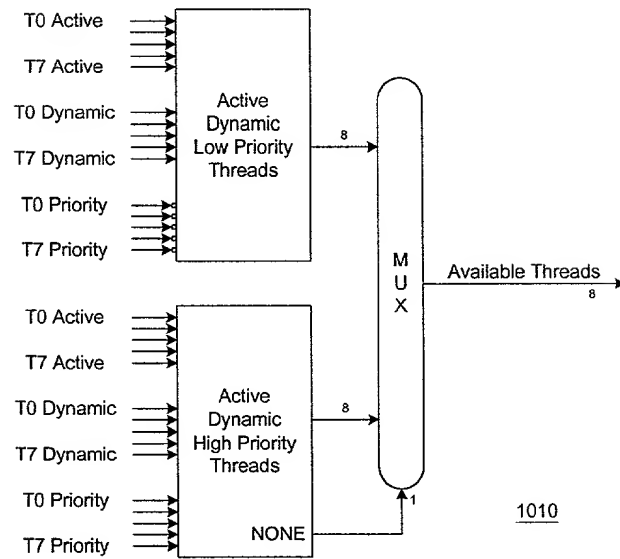


Figure 11

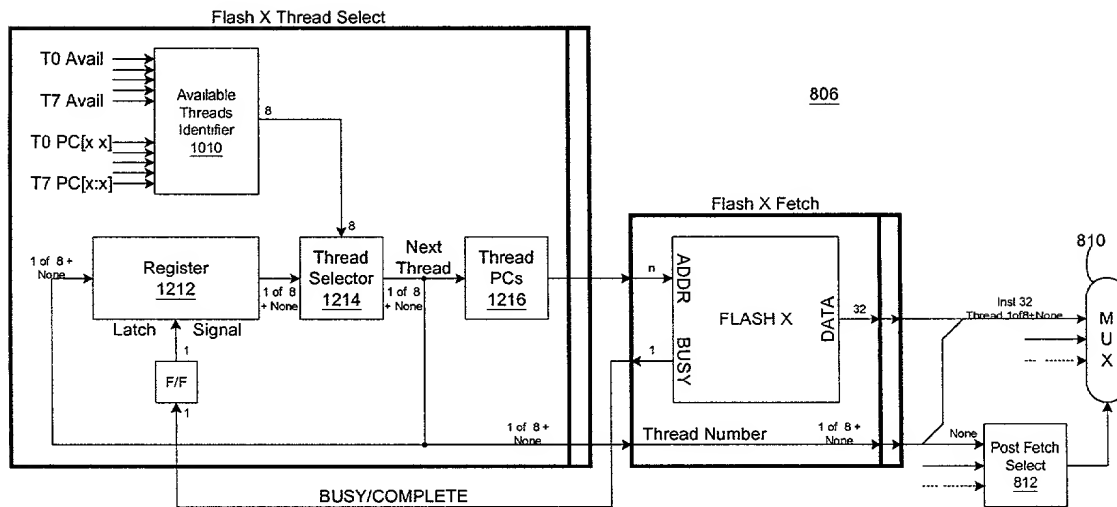


Figure 12

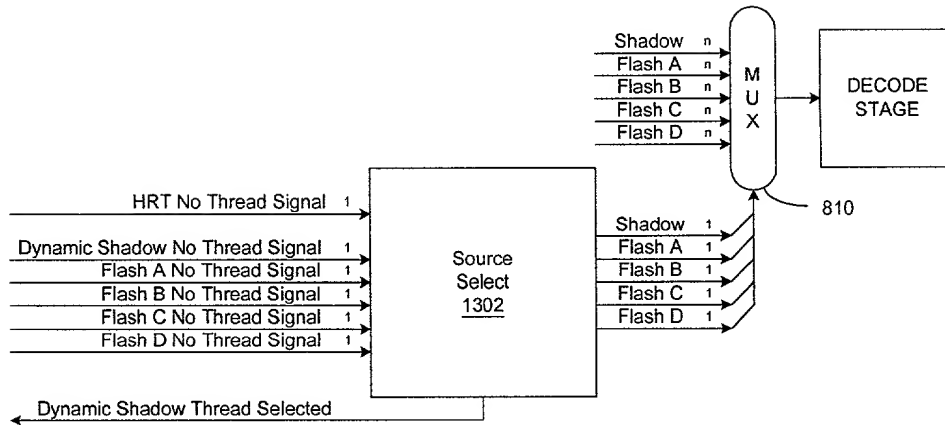


Figure 13

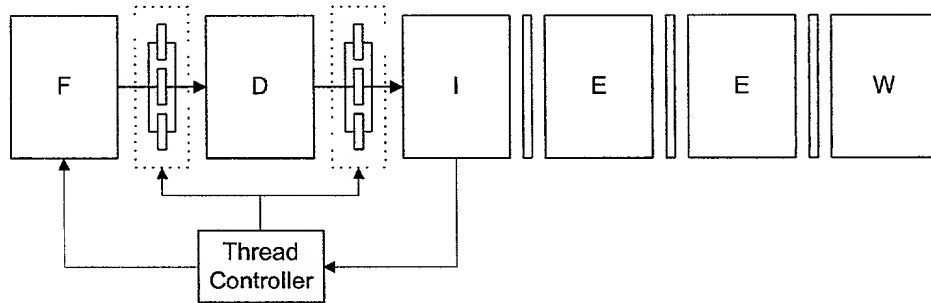


Figure 14

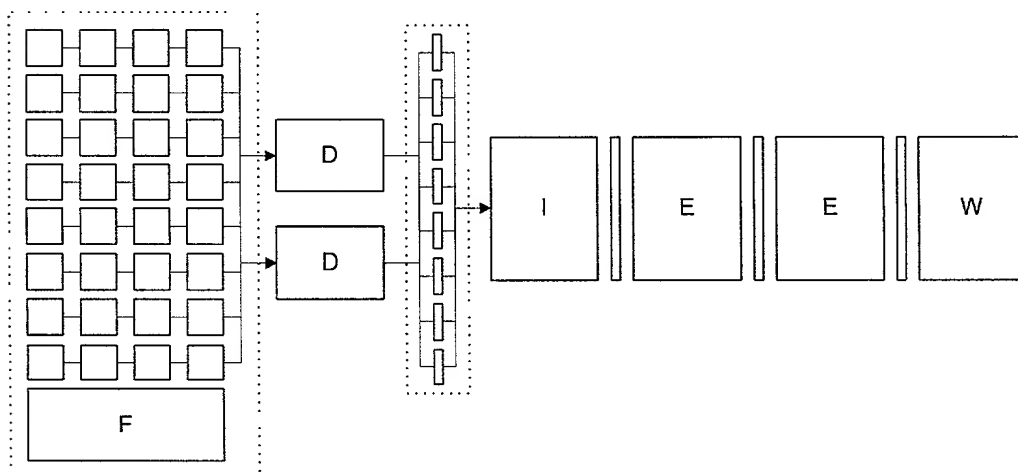


Figure 15

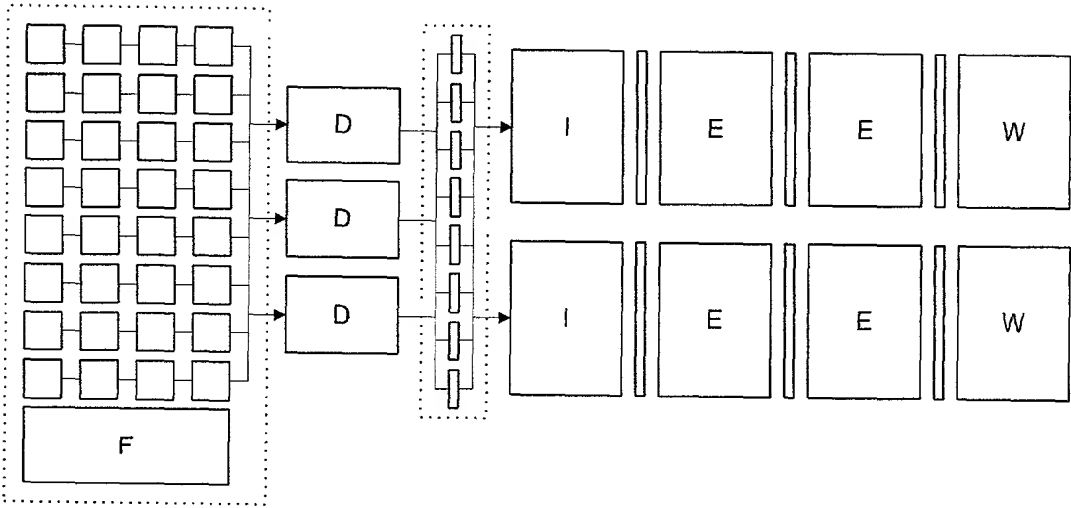


Figure 16

FIG. 16